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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Bhupendra Sharma, et al.

Docket No.: TI-38242

Serial No.: 10/711,532

Art Unit: 2816

Filed: September 24, 2004

Examiner: Anh Quan Tra

Customer No. 23494

Conf. No.: 5531

For: Avoiding Excessive Cross-Terminal Voltages of Low Voltage Transistors Due to
Undesirable Supply-Sequencing in Environments With Higher Supply Voltages

CERTIFICATION OF FACSIMILE TRANSMISSION

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<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT <u>(# Page)</u>
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Bhupendra Sharma, et al.	
TITLE OF INVENTION: Avoiding Excessive Cross-Terminal Voltages of Low Voltage Transistors Due to Undesirable etc.	
TI FILE NO.: TI-38242	DEPOSIT ACCT. NO.: 20-0668
FAXED: 1/5/2006 DUE: 1/13/2006 ATTY/SECY: AZ/ms	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/711,532 Filing Date: September 24, 2004	

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	Bhupendra Sharma, et al.	Docket No.: TI-38242
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT PURSUANT TO 37 CFR 1.111

Dear Sir:

This amendment is in response to the Office Action dated October 13, 2005, having a statutory period for response set to expire on January 13, 2006.

Amendments to Claims begin on page 2.

Remarks/Arguments begin on page 6.

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